



# 1<sup>ST</sup> ANNOUNCEMENT & CALL FOR PAPERS

ISPSD is the premier forum for technical discussion in all areas of power semiconductor devices and power integrated circuits. ISPSD 2024 will be held in the concert hall 'Die Glocke' in the Free Hanseatic City of Bremen, the cosmopolitan town on the river Weser, home of the famous Bremen Town Musicians, full of history with a UNESCO World Heritage site and a European centre of aeronautics and space industry.

## CONFERENCE TRACKS

**High Voltage Power Devices (HV):** High voltage silicon based discrete devices ( $> 200$  V) such as super junction MOSFETs, IGBTs, thyristors, GTOs and pn-diodes

**Low Voltage Power Devices and Power IC Technology (LVT):** Low voltage silicon based discrete power devices ( $\leq 200$  V) and power devices for power ICs of all voltage ranges

**Power IC Design (ICD):** Circuit design and demonstration using power IC technology platform

**GaN and III/V Compound Materials (GaN):** GaN and other III/V compound material (e.g. AlN, GaAs) based power devices, technology and integration, materials, and processing

**SiC and Other Materials (SiC):** SiC and other material (e.g.  $\text{Ga}_2\text{O}_3$ , diamond) based power devices, technology and integration, materials, and processing

**Module and Package Technologies (PK):** Package technology for modules, discrete power devices and power ICs



## ABSTRACT SUBMISSION

**Abstract Submission Deadline:**  
December 4, 2023

**Author Notification:**  
February 5, 2024

**Late News Submission (limited acceptance):**  
March 8, 2024

**Final Manuscript Submission Deadline:**  
March 22, 2024

### Submission Requirements:

A PDF abstract should be submitted through the website including a single page text summary in English (500 words maximum) and up to two additional pages of supporting figures.

### GENERAL CHAIR:

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